

Abstract

In a switching controller whose error signal amplifier (KO) essentially exhibits P action, the load step performance of .
5 the switching controller is detected via an evaluation circuit (A). This signal is fed directly to the pulse-duration or pulse-frequency modulator (PBM) for the final controlling element (S1), substantially circumventing the error signal amplifier (KO) for the switching controller.

10 Through this measure, output capacities of the switching controller can be reduced without stability problems.

15 Figure 1